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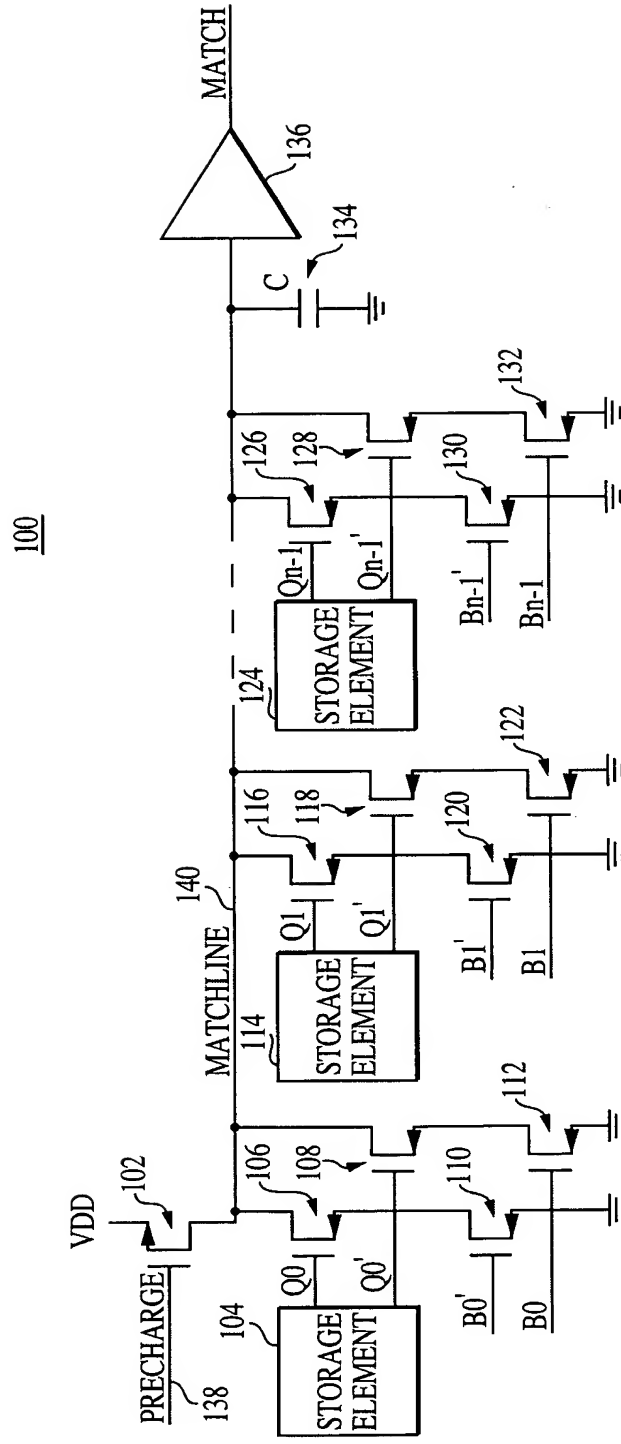


FIG. 1
PRIOR ART

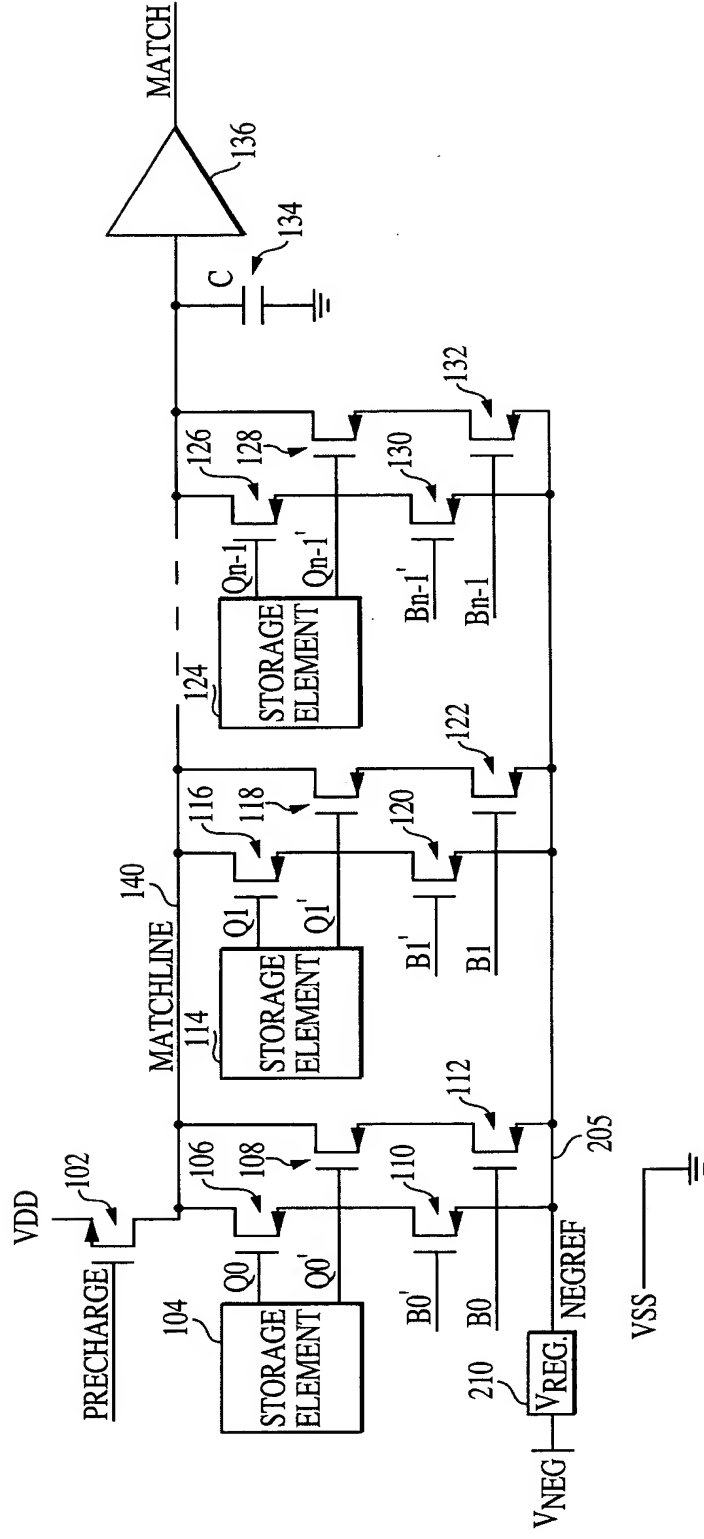


FIG. 2

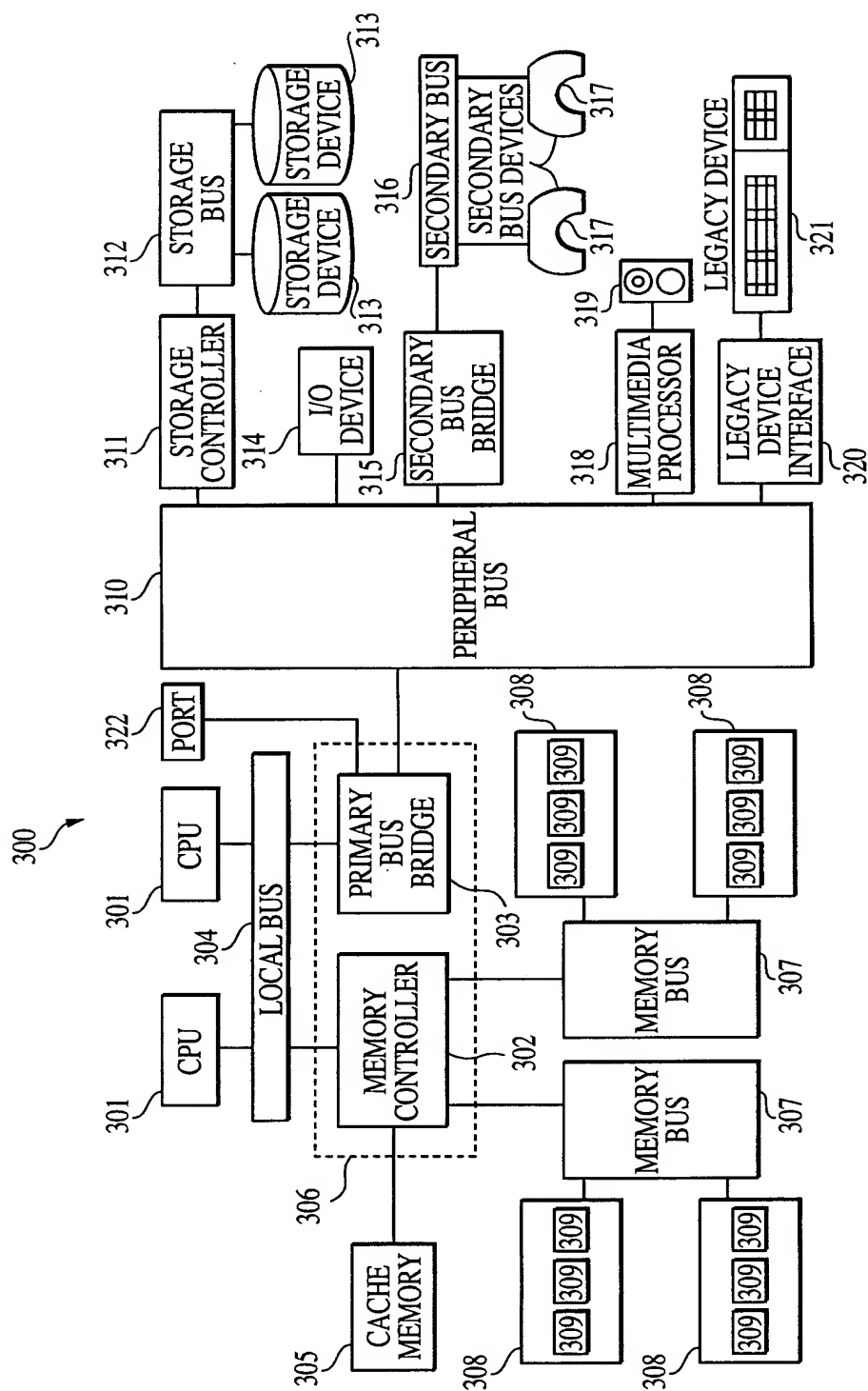


FIG. 3

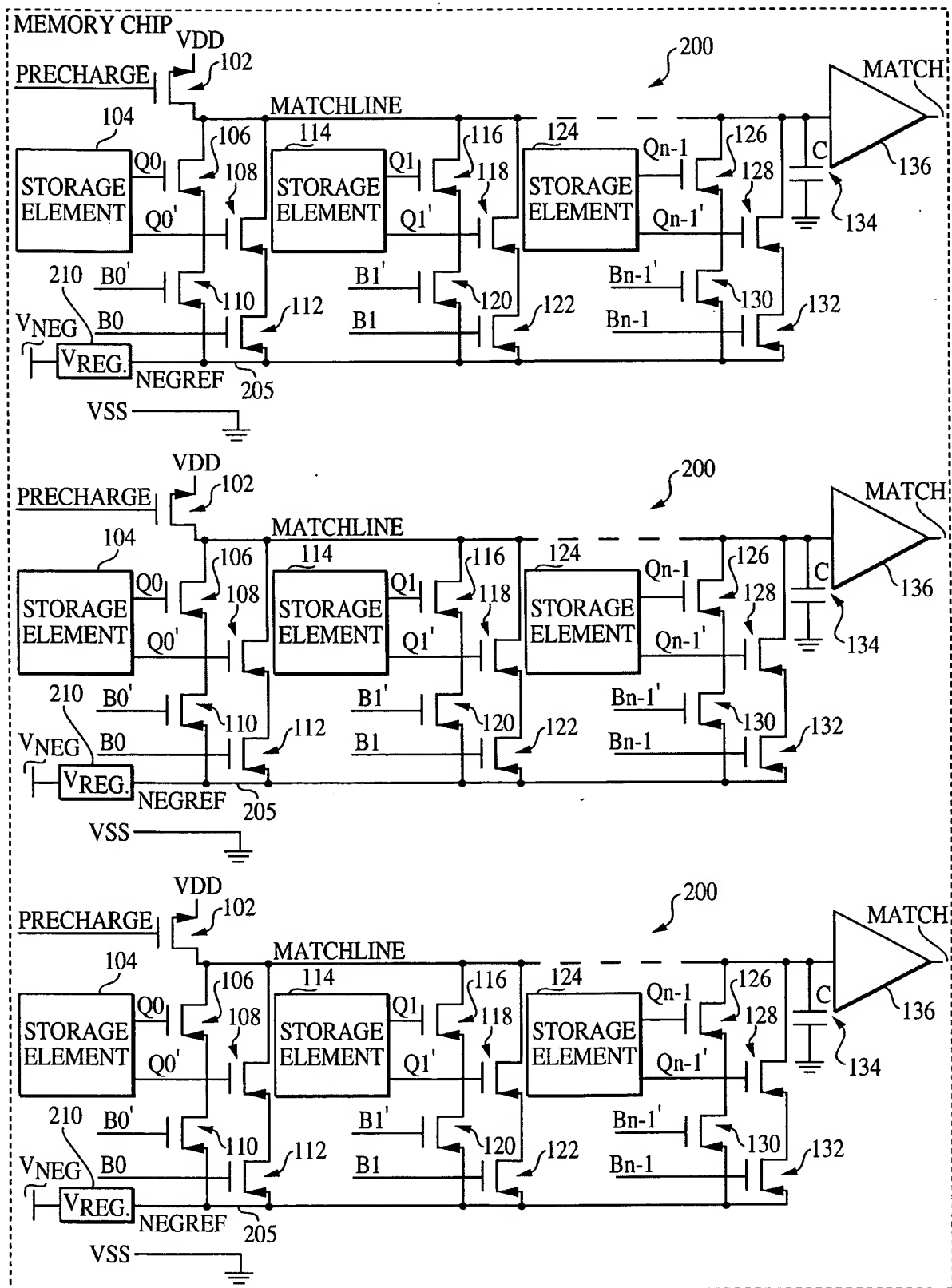


FIG. 4

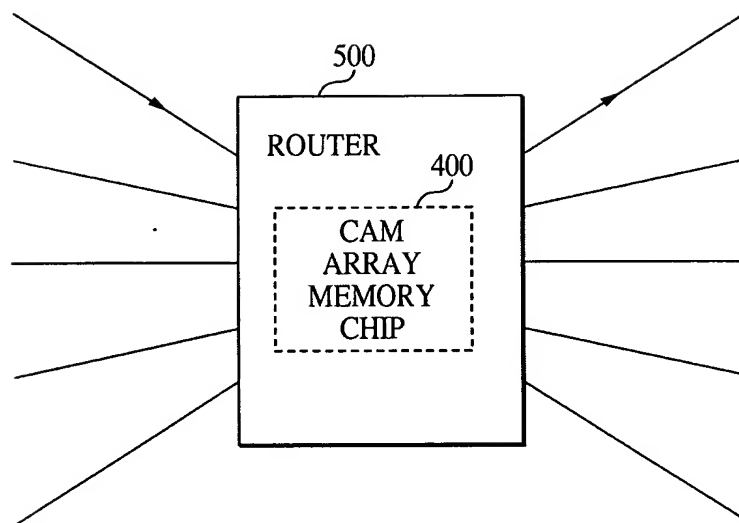


FIG. 5

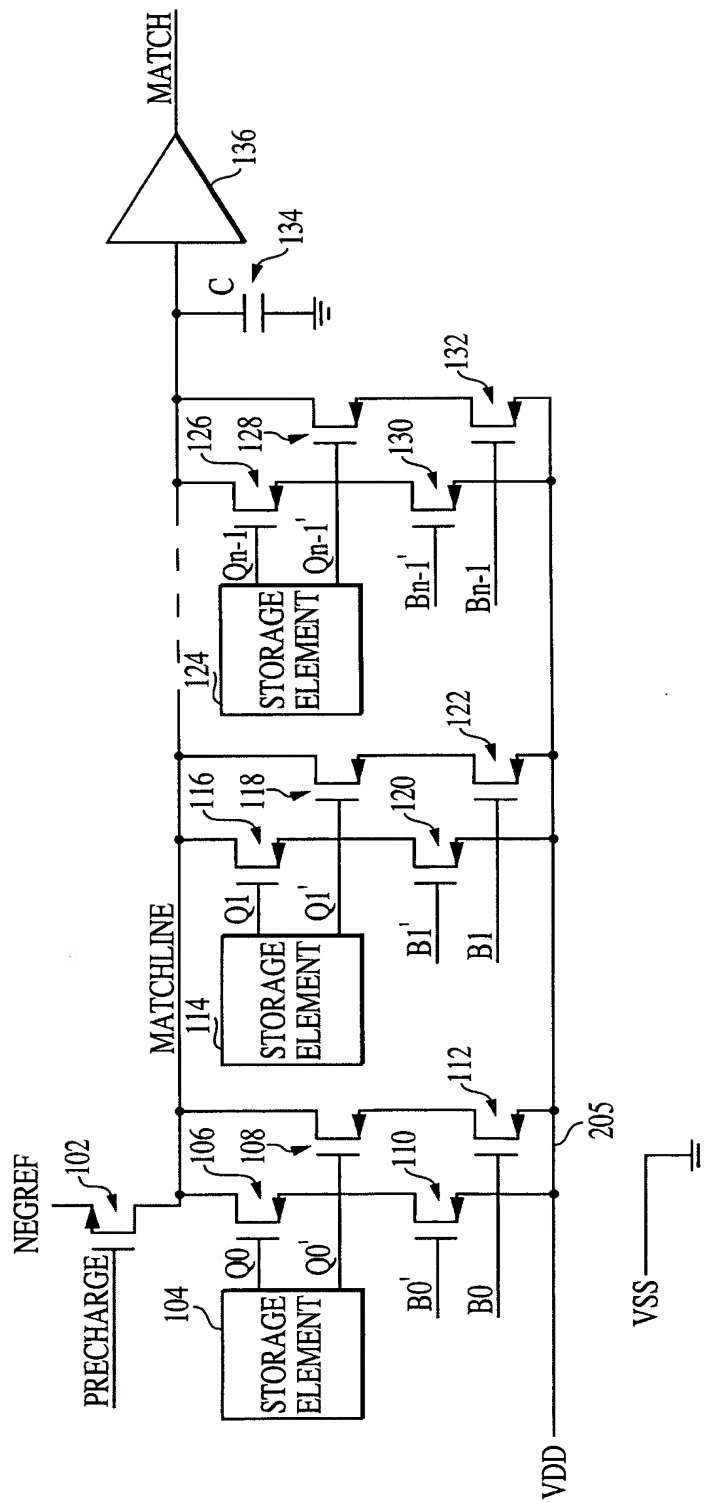


FIG. 6

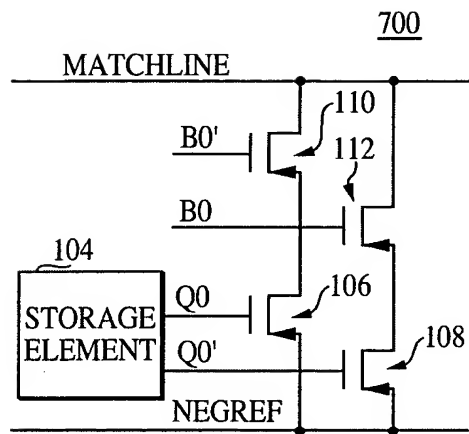


FIG. 7

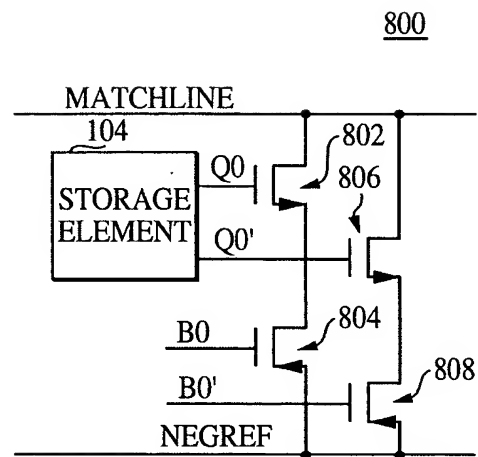


FIG. 8

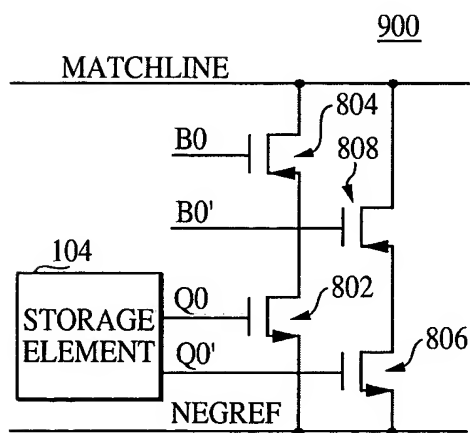


FIG. 9

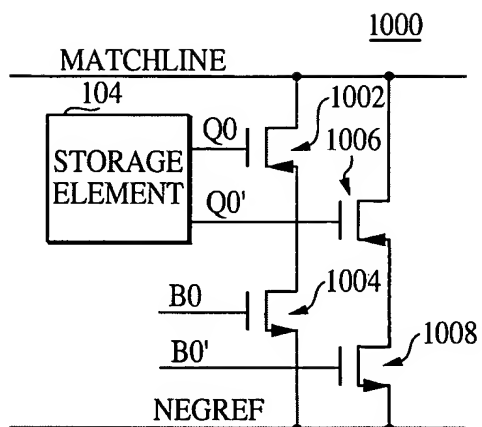


FIG. 10

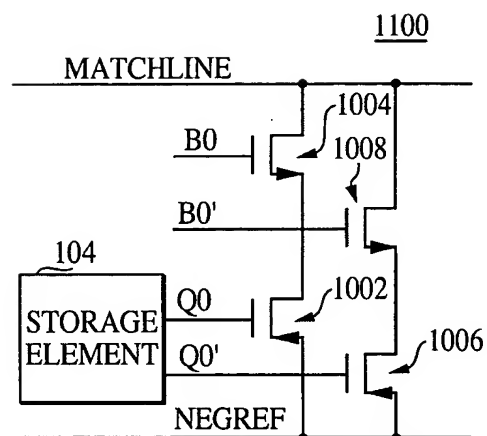


FIG. 11

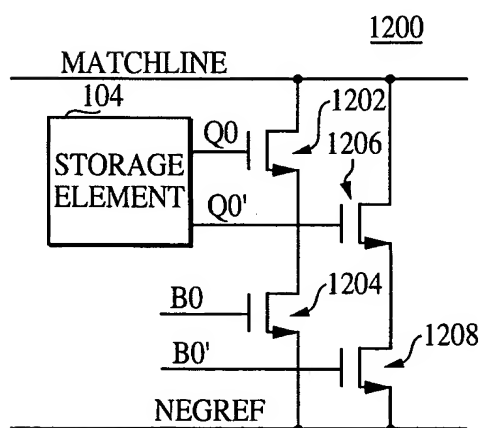


FIG. 12

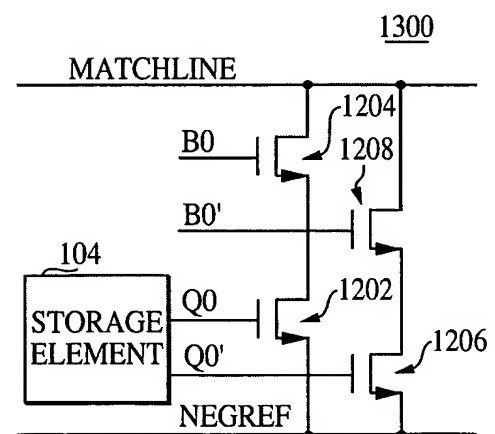


FIG. 13

100

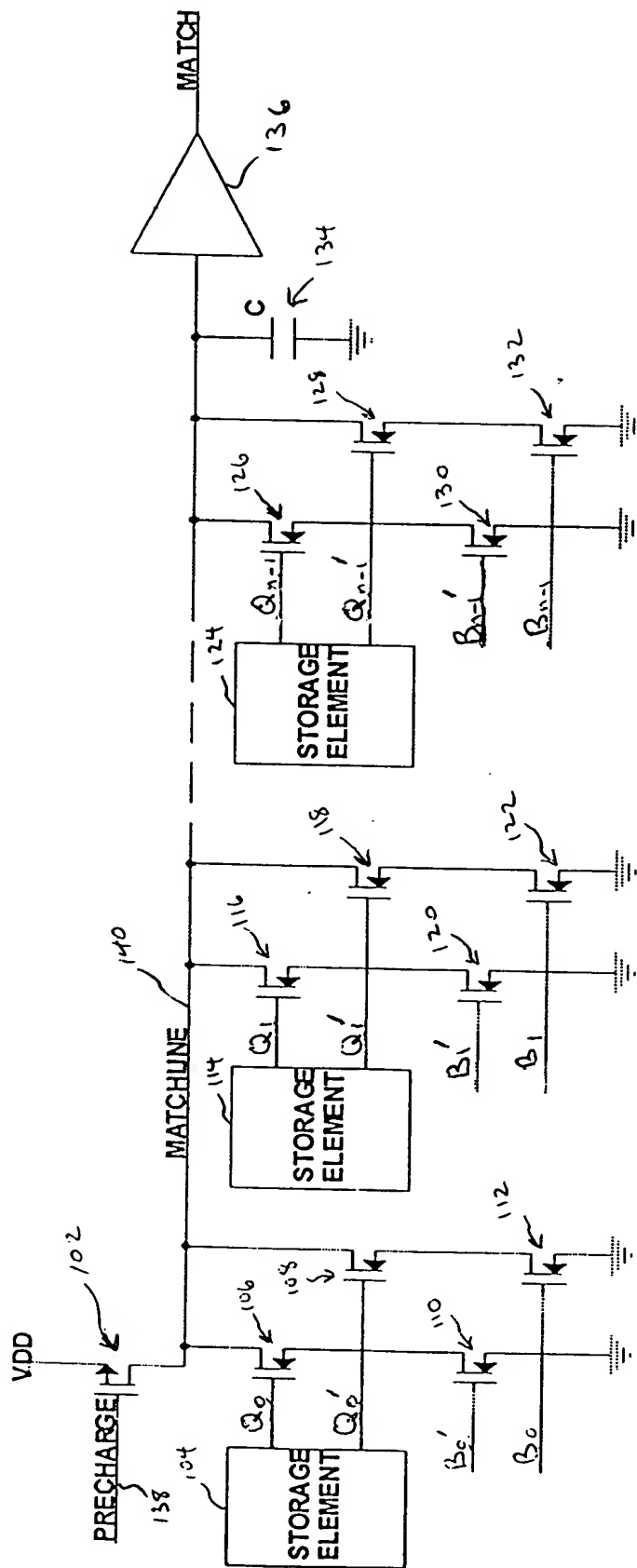


FIG. 1
Prior Art

200

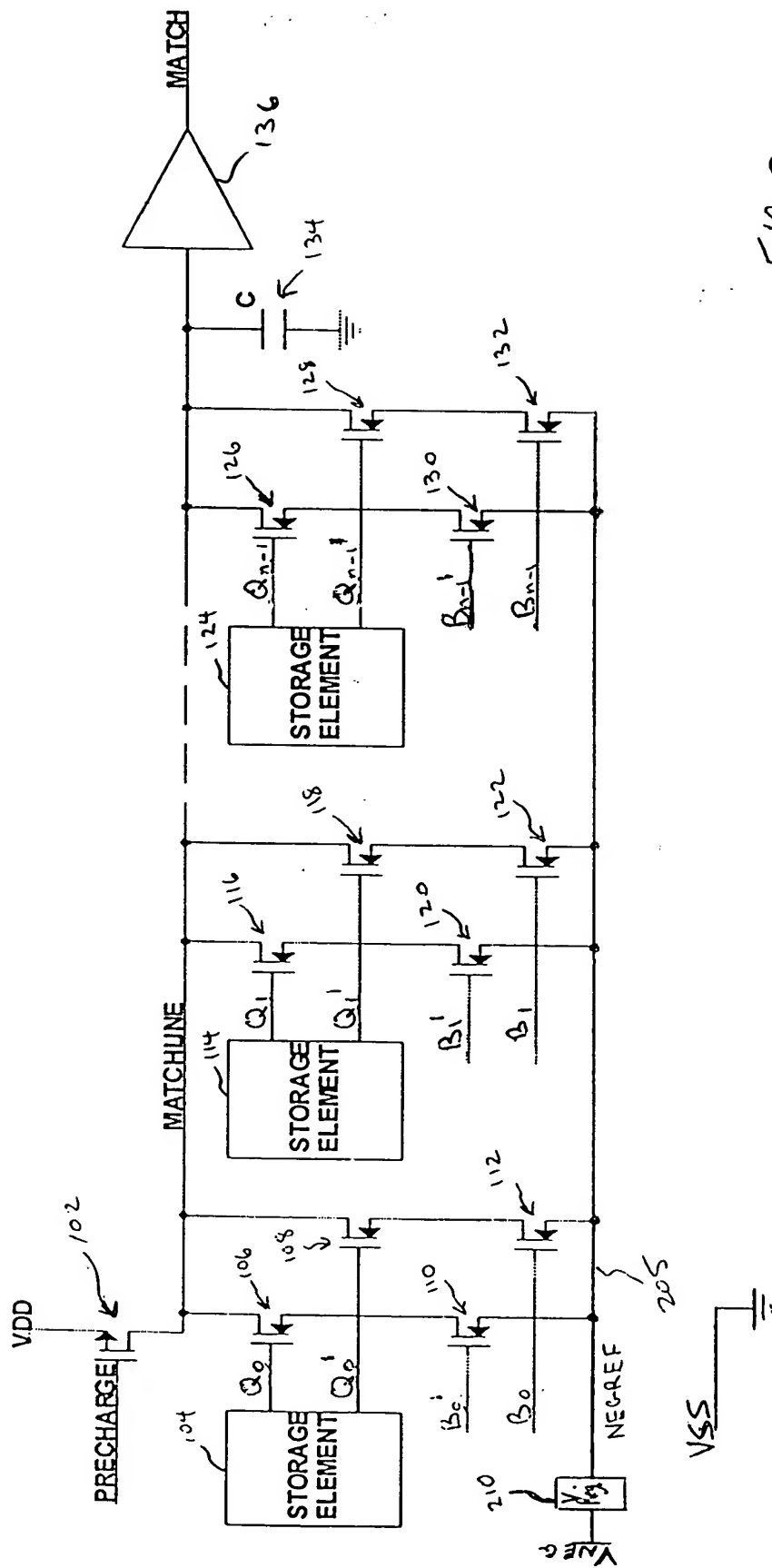
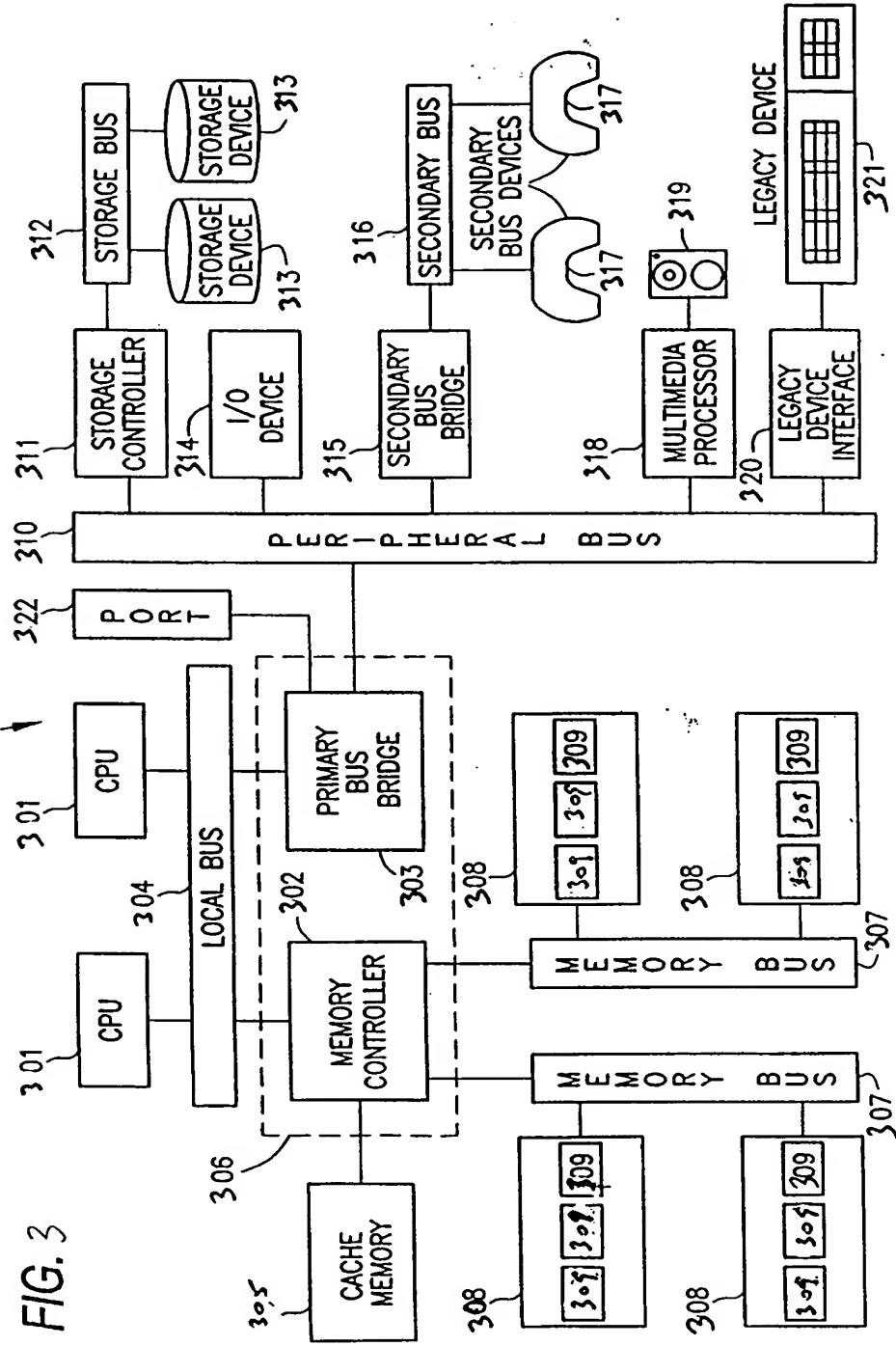


FIG. 2

300

FIG. 3



400
Memory chip

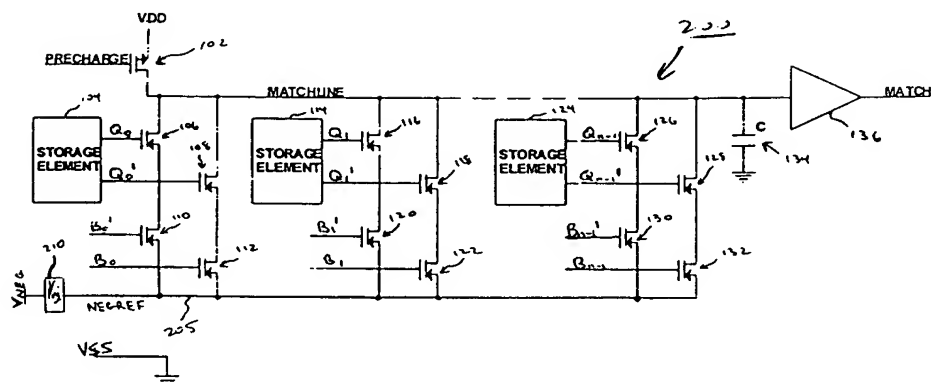
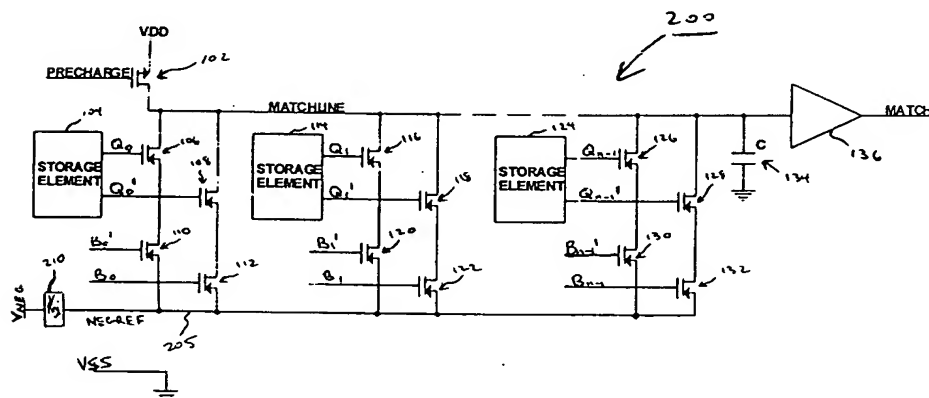
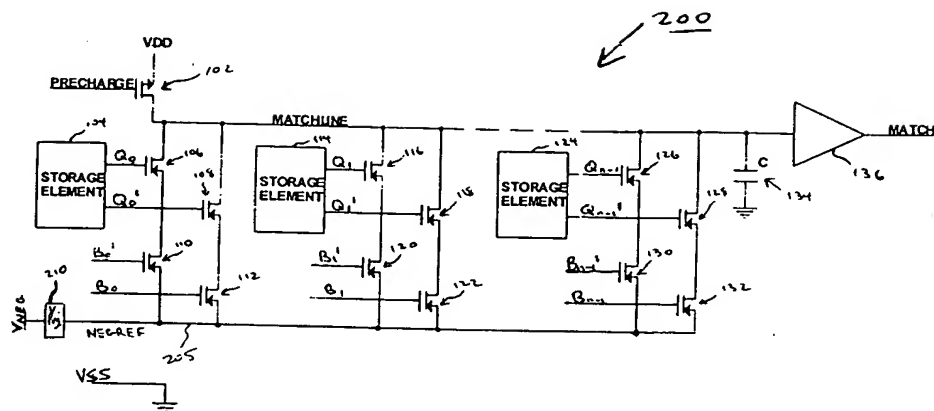


FIG. 4

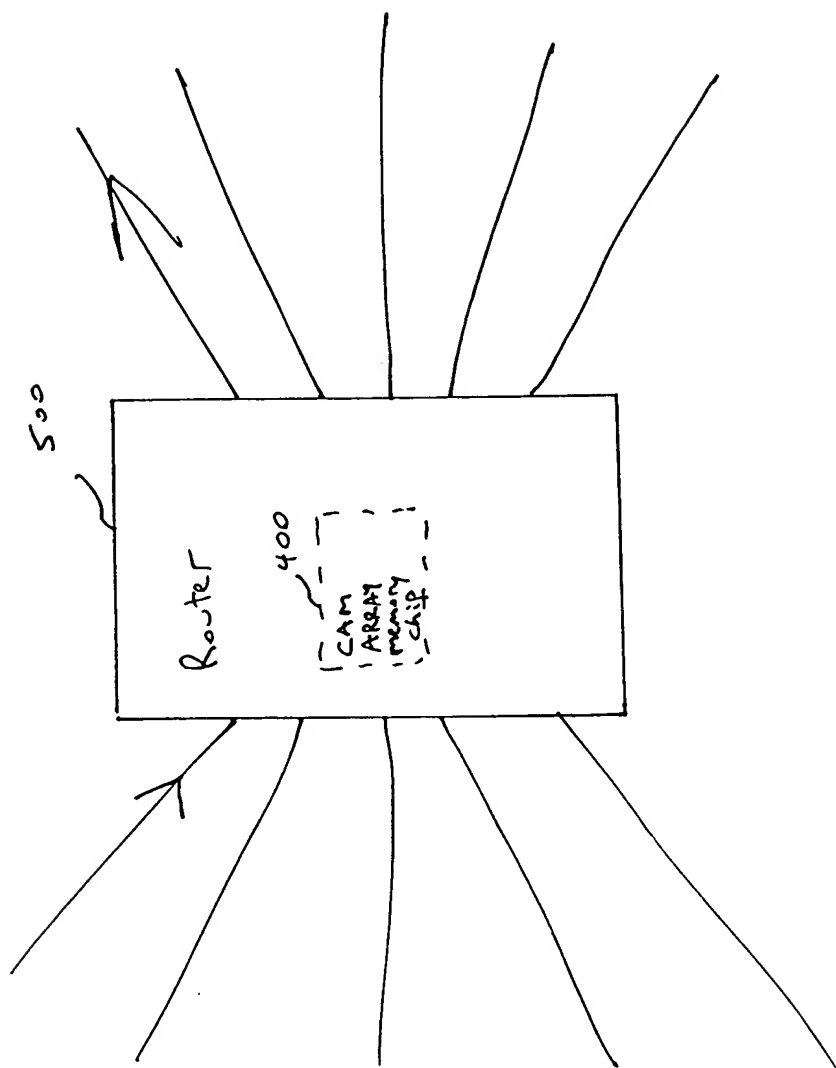


FIG. 5

600

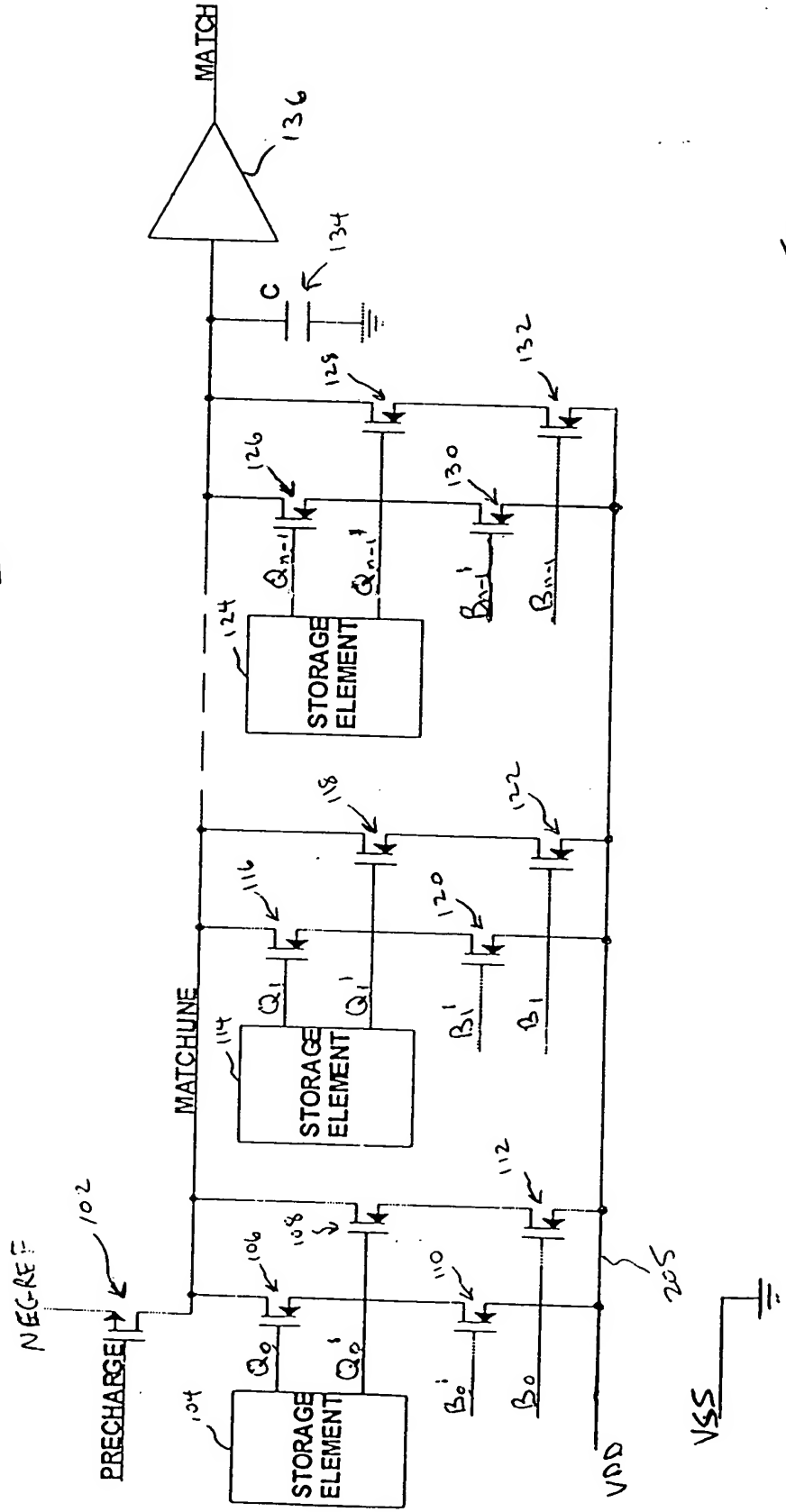


FIG. 6

